FIG. 1

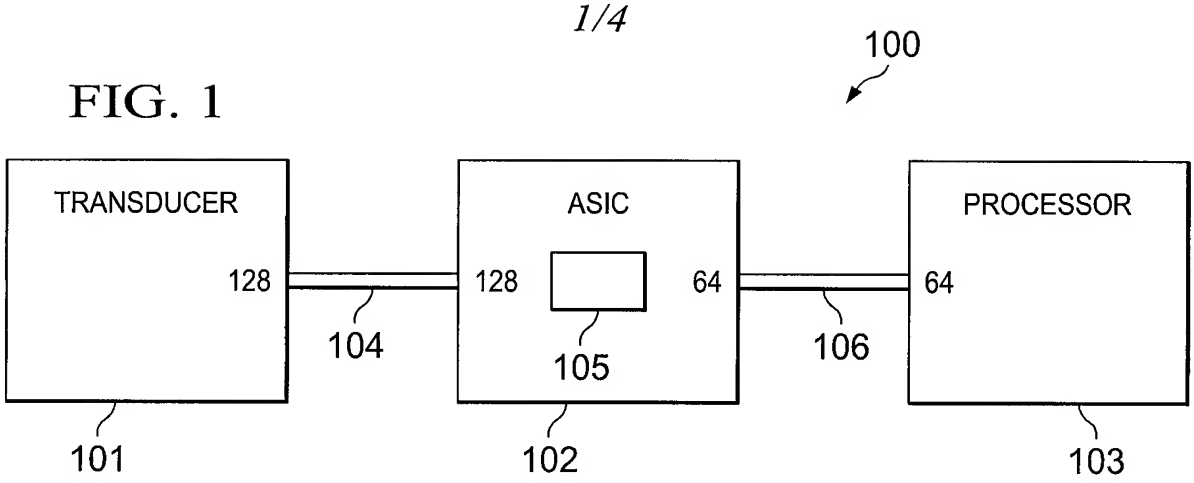
100
TRANSDUCER
128
ASIC
128
64
PROCESSOR
64
104
105
106
101
102
103

FIG. 2A

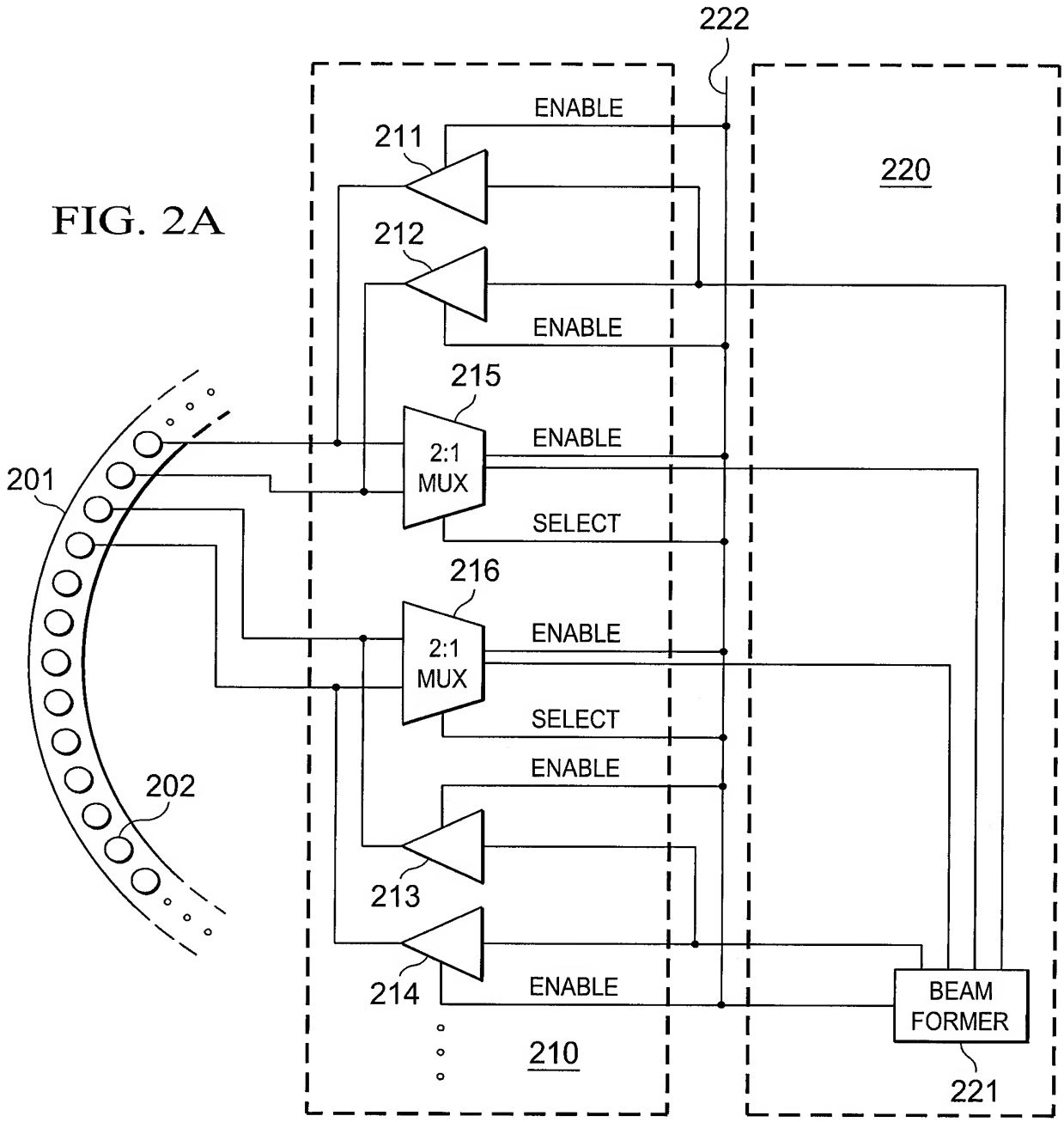
222
ENABLE
211
220
212
ENABLE
215
ENABLE
2:1
MUX
201
SELECT
216
ENABLE
2:1
MUX
SELECT
ENABLE
202
213
214
ENABLE
BEAM
FORMER
210
221

FIG. 2B

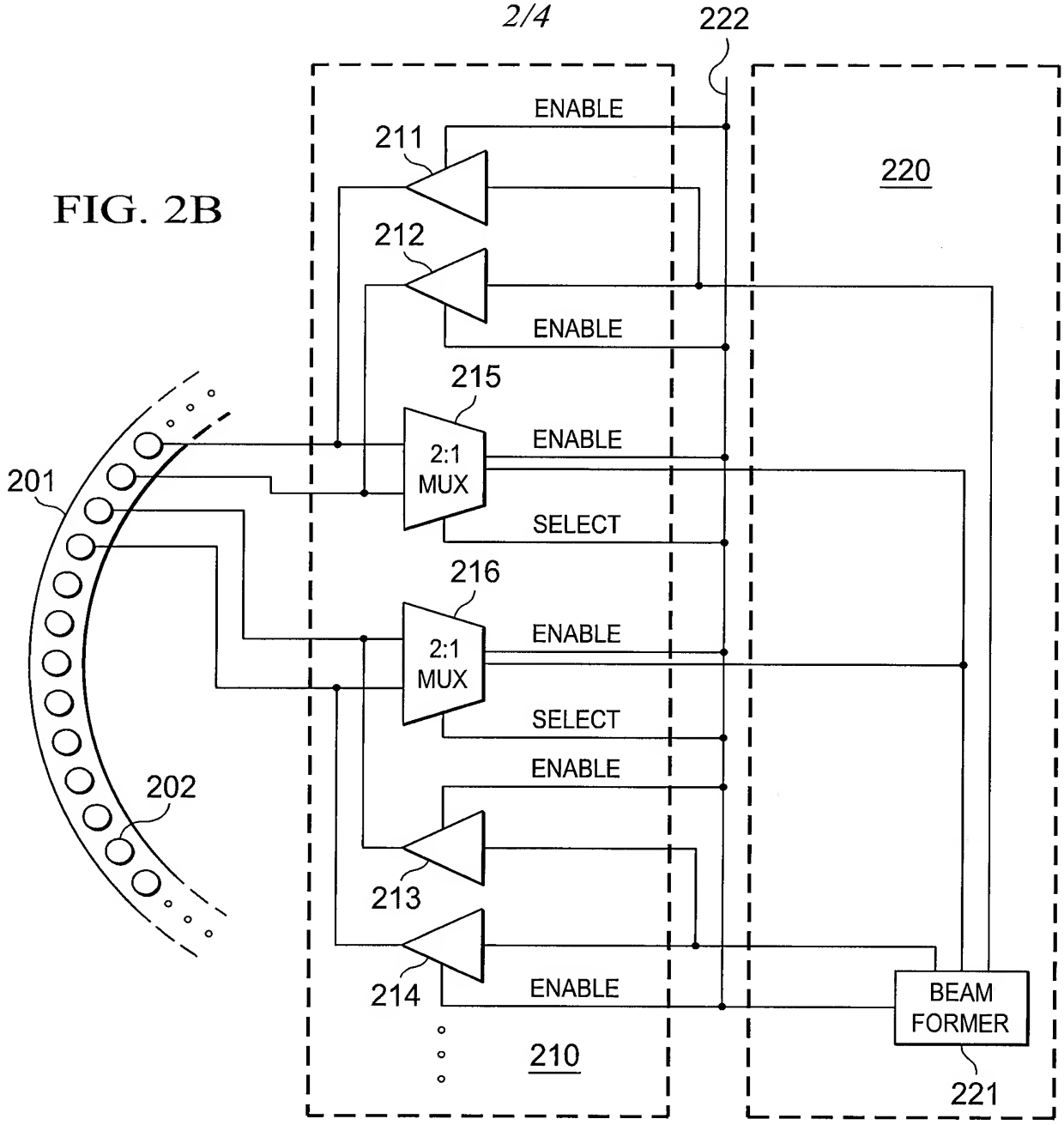
222
ENABLE
211
220
212
ENABLE
215
2:1
MUX
ENABLE
201
SELECT
216
2:1
MUX
ENABLE
SELECT
ENABLE
202
213
214
ENABLE
BEAM
FORMER
210
221

FIG. 3A

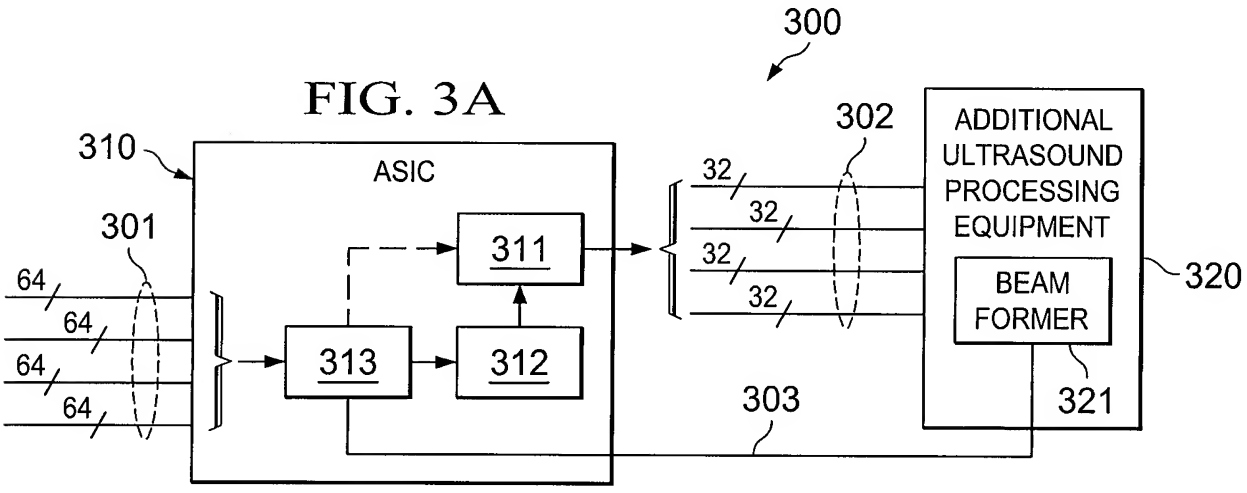
300
310
ASIC
301
311
64
64
64
64
313
312
32
32
32
32
302
ADDITIONAL
ULTRASOUND
PROCESSING
EQUIPMENT
BEAM
FORMER
320
321
303

FIG. 3B

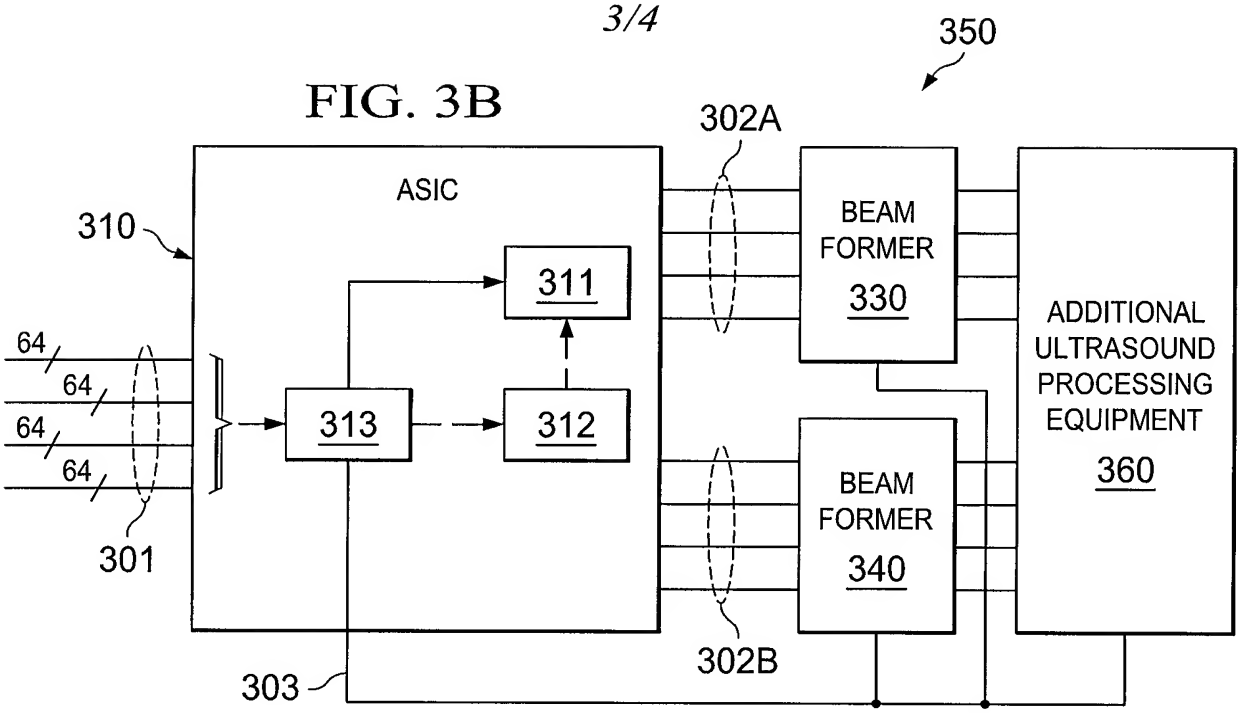

FIG. 4

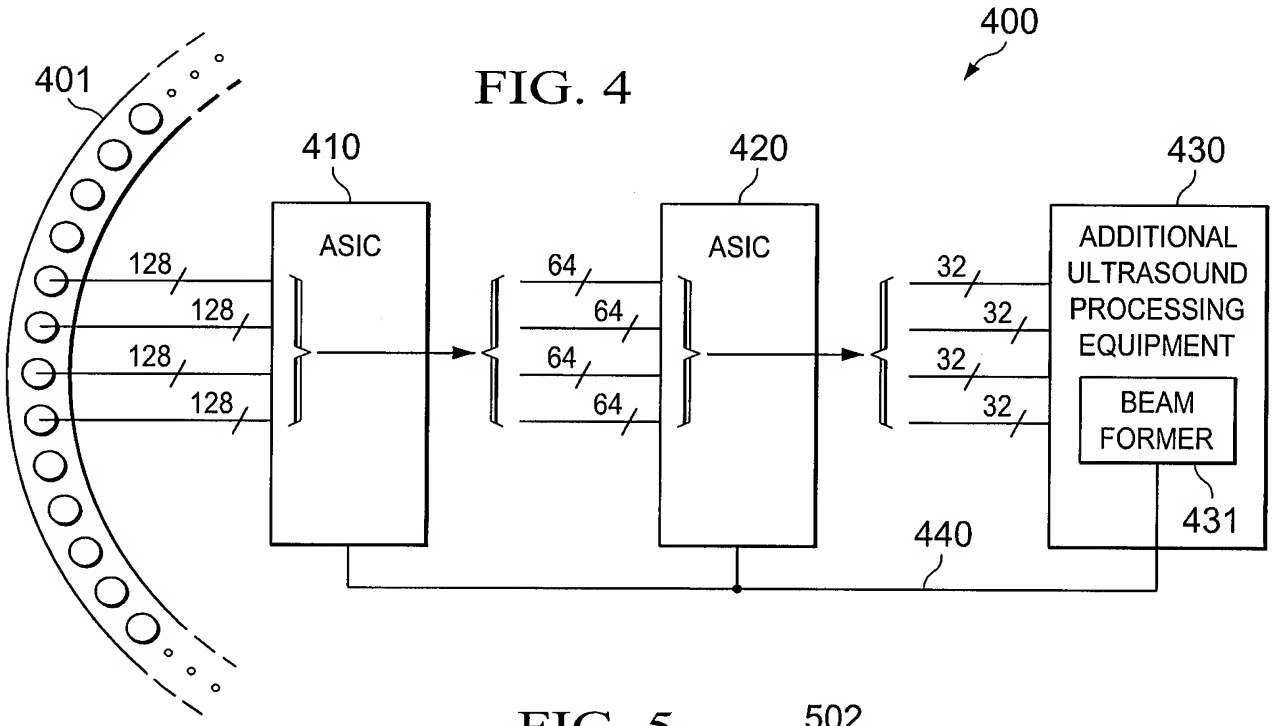

FIG. 5

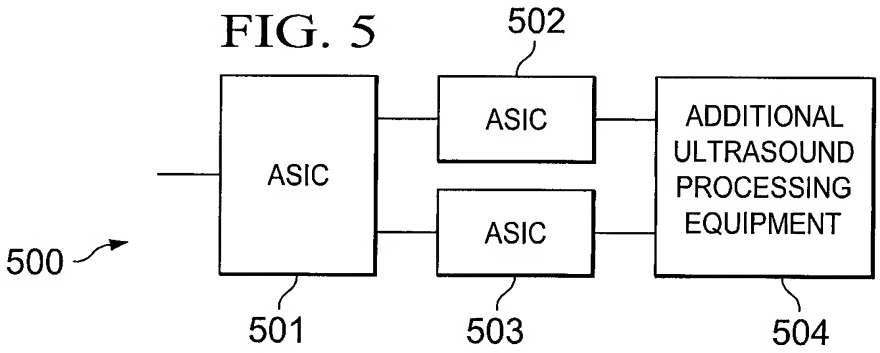

FIG. 6

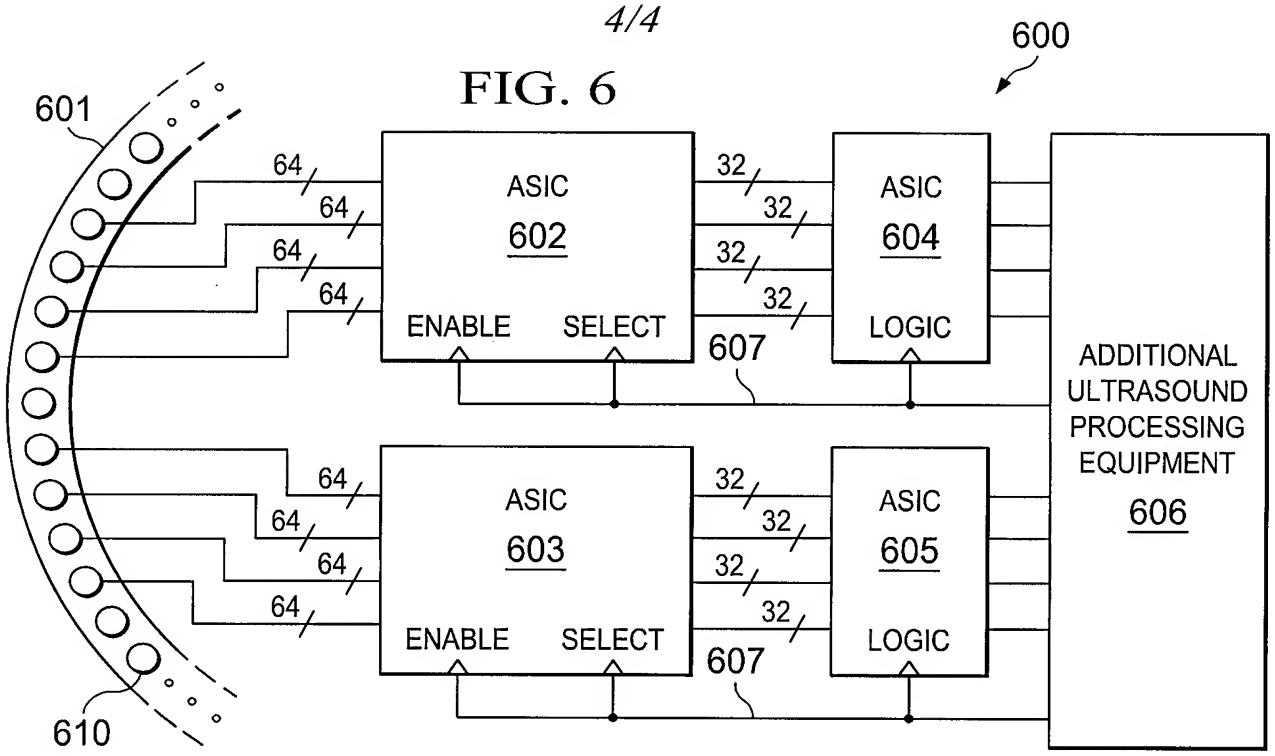

FIG. 7

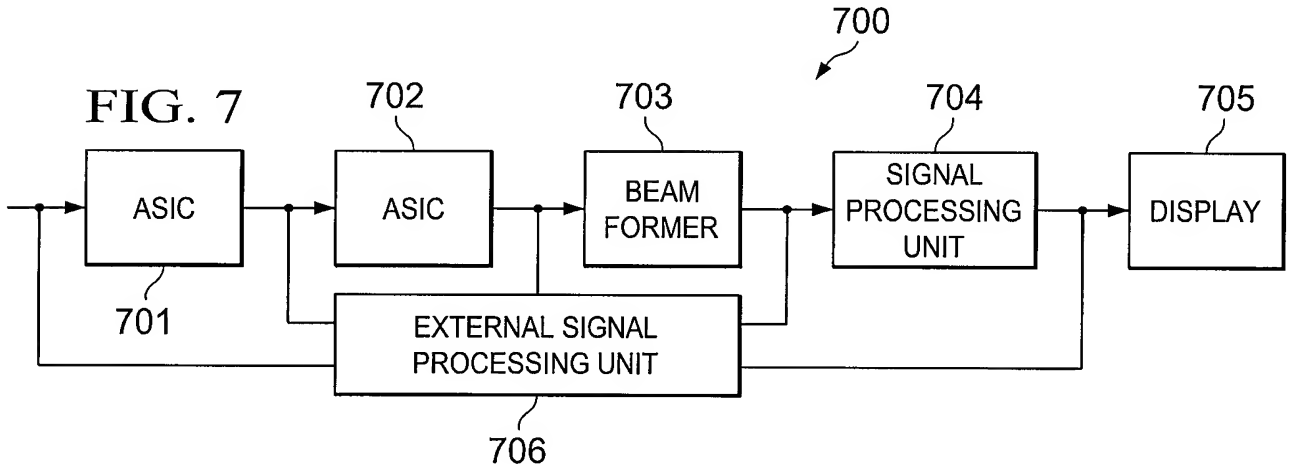

FIG. 8

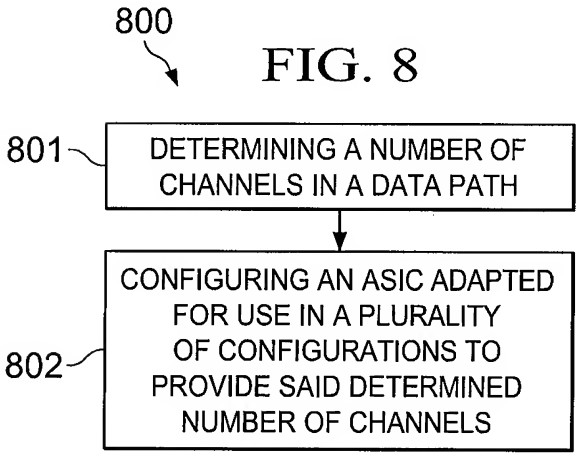
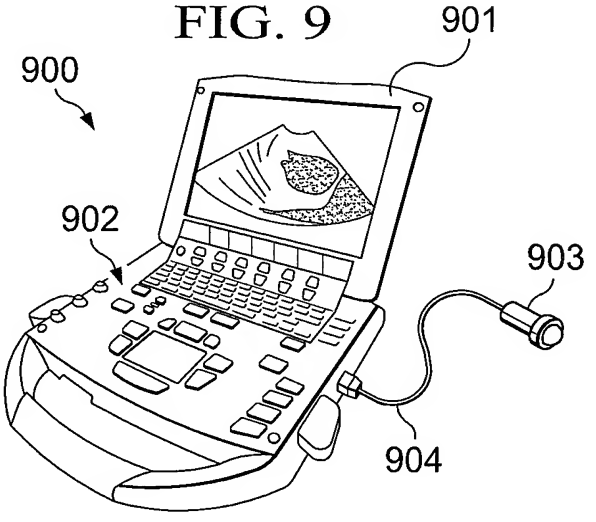
FIG. 9